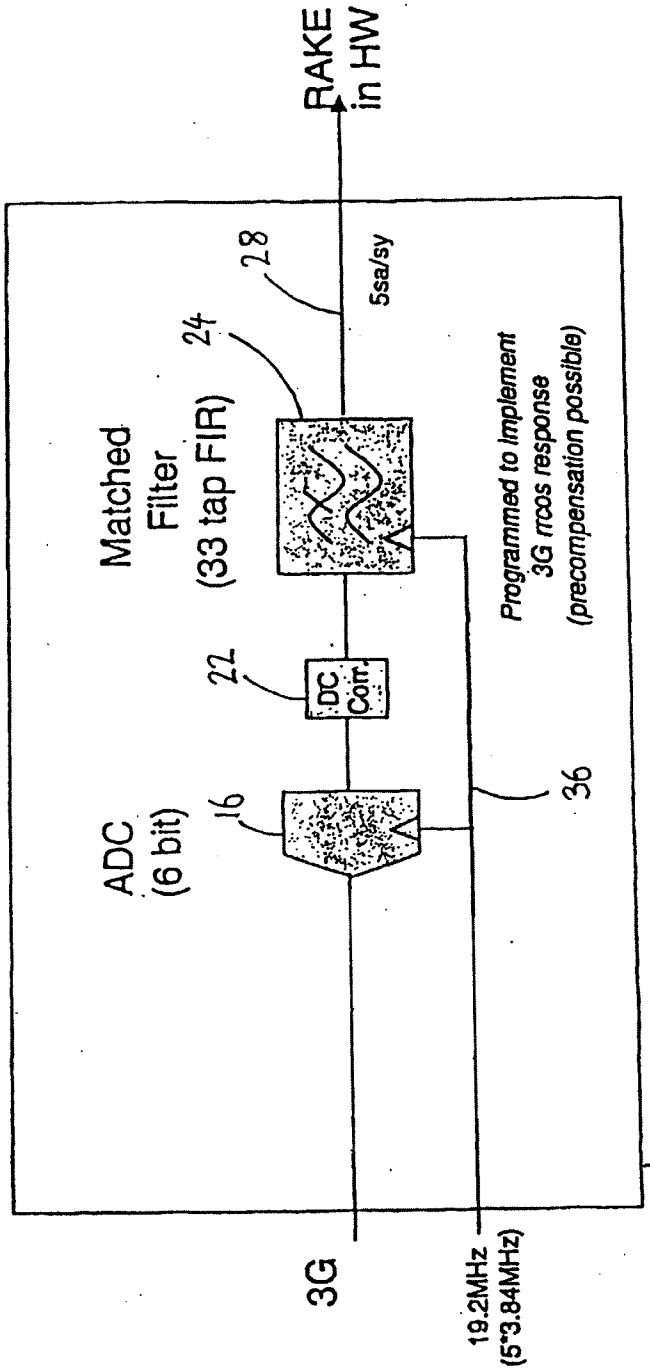
RAKE
in HW
28
5sa/sy
24
Matched
Filter
(33 tap FIR)
Programmed to implement
3G rrcos response
(precompensation possible)
22
DC
Corr.
ADC
(6 bit)
16
36
3G
19.2MHz
(5*3.84MHz)
10

Figure 2